

41
Docket No.: 49657-875

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

PATENT

In re Application of

Tetsushi TANIZAKI, et al.

Application No.: 09/712,246

Filed: November 15, 2000

For: SEMICONDUCTOR MEMORY DEVICE WITH BUILT-IN SELF TEST CIRCUIT OPERATING AT HIGH RATE

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Customer Number: 20277

Confirmation Number: 8725

Group Art Unit: 2133

Examiner: M.M. Chaudry

RECEIVED

OCT 20 2004

Technology Center 2100

Dear Sir:

Transmitted herewith is an Amendment in the above-identified application.

- ☒ No additional fee is required.
☐ Applicant is entitled to small entity status under 37 CFR 1.27
☐ Also attached:

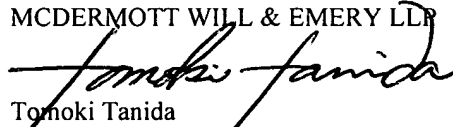
The fee has been calculated as shown below:

	NO. OF CLAIMS	HIGHEST PREVIOUSLY PAID FOR	EXTRA CLAIMS	RATE	FEE
Total Claims	20	20	0	\$18.00 =	\$0.00
Independent Claims	3	3	0	\$88.00 =	\$0.00
Multiple claims newly presented					\$0.00
Fee for extension of time					\$0.00
					\$0.00
Total of Above Calculations					\$0.00

- ☐ Please charge my Deposit Account No. 500417 in the amount of \$0.00. An additional copy of this transmittal sheet is submitted herewith.
- ☒ The Commissioner is hereby authorized to charge payment of any fees associated with this communication or credit any overpayment, to Deposit Account No. 500417, including any filing fees under 37 CFR 1.16 for presentation of extra claims and any patent application processing fees under 37 CFR 1.17.

Respectfully submitted,

MCDERMOTT WILL & EMERY LLP


Tomoki Tanida
Recognition Under 37 CFR 10.9(b)

600 13th Street, N.W.
Washington, DC 20005-3096
202.756.8000 SAB:TT:lnm
Facsimile: 202.756.8087
Date: October 19, 2004
WDC99 992651-1.049657.0875



Docket No.: 49657-875

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of	:	Customer Number: 20277
	:	
Tetsushi TANIZAKI, et al.	:	Confirmation Number: 8725
	:	
Application No.: 09/712,246	:	Group Art Unit: 2133
	:	
Filed: November 15, 2000	:	Examiner: M.M. Chaudry
	:	
For: SEMICONDUCTOR MEMORY DEVICE WITH BUILT-IN SELF TEST CIRCUIT OPERATING AT HIGH RATE	:	

RECEIVED

OCT 20 2004

REQUEST FOR RECONSIDERATION

Technology Center 2100

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Reconsideration of the rejection imposed in the Office Action dated July 19, 2004, is solicited in light of the following Remarks.